

CAPS Version	Release Date	Comments
2.40	January 2007	<ul style="list-style-type: none"> ▪ JTAG: <ul style="list-style-type: none"> - RLINK: Failed to detect IDCODE on multiple device JTAG chain with non-ST devices and uPSD devices. Issue resolved ▪ Installation: <ul style="list-style-type: none"> - FlashLINK driver: Installation process halted when installing CAPS on newer Windows XP distribution is now resolved
2.30	November 2006	<ul style="list-style-type: none"> ▪ JTAG: <ul style="list-style-type: none"> - uPSD325x: The new 854T2 die IDCODE (18540041) is now added to the ISP library ▪ PSDsoft project can be imported to CAPS without PSDsoft being installed on the same PC
2.20	September 2006	<ul style="list-style-type: none"> ▪ This release supports the following STR9 devices: <div style="display: flex; flex-wrap: wrap;"> <div style="width: 50%;">STR910FAM32X6</div> <div style="width: 50%;">STR910FAW32X6</div> <div style="width: 50%;">STR910FAZ32H6</div> <div style="width: 50%;">STR912FAW34X6</div> <div style="width: 50%;">STR911FAM42X6</div> <div style="width: 50%;">STR912FAW42X6</div> <div style="width: 50%;">STR912FAZ42H6</div> <div style="width: 50%;">STR911FAM44X6</div> <div style="width: 50%;">STR912FAW44X6</div> <div style="width: 50%;">STR912FAZ44H6</div> </div> ▪ STR9: <ul style="list-style-type: none"> - Modification: PLL frequency entry validation is now change to between 6.25-96 MHz (was between OSC frequency and 96 MHz) - Bug on Header file: Timer DEFINE variables is now assigned as TIM_x (was TMR_x) - Bug on 128-pin package: SSP_mstr_sel_out pin is now correctly assigned to P1.3 (was P1.4)
2.10	July 2006	<ul style="list-style-type: none"> ▪ JTAG: <ul style="list-style-type: none"> - STR9: FlashLINK programming issue is now resolved - STR9: RLink is now supported - More than one regions can now be selected for any programming operation ▪ STR9: <ul style="list-style-type: none"> - Peripheral assignment bug: For USB peripheral, when 96 MHz is selected for PLL clock, it incorrectly reports internal clock/2 is not available. Fixed. - Peripheral assignment: It is now reporting which peripheral(s) caused the pin conflict during the assignment - Design can now be re-targeted to different package and/or memory density. Pin/Peripheral re-assignment depends on target device resource availability
2.00	May 2006	<ul style="list-style-type: none"> ▪ This release supports the following STR9 devices: <div style="display: flex; flex-wrap: wrap;"> <div style="width: 50%;">STR910FM32X6</div> <div style="width: 50%;">STR910FW32X6</div> <div style="width: 50%;">STR911FM42X6</div> <div style="width: 50%;">STR912FW42X6</div> <div style="width: 50%;">STR911FM44X6</div> <div style="width: 50%;">STR912FW44X6</div> </div>
1.00	March 2006	<p>Note: Please recompile and refit project created using the Beta version</p> <ul style="list-style-type: none"> ▪ This new software supports all PSD families ▪ Project created using PSDsoft Express V8.0 or later can be imported to CAPS design environment

